

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: FUJISAWA et al.
Filed: July 3, 2003
For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE
(AS AMENDED)

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 AND 1.98

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 3, 2003

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with prior applications of the above-identified application, that is, prior Application No. 10/214,313, filed August 8, 2002; Application No. 09/594,046, filed June 15, 2000; and Application No. 09/126,438, filed July 30, 1998.

Since each of No. 10/214,313, No. 09/594,046 and No. 09/126,438 is being relied upon under 35 USC 120 in the above-identified application, copies of the listed documents are not enclosed. See 37 CFR 1.98(d).

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified application. Clearly, requirements of 37 CFR 1.97(b) are satisfied.

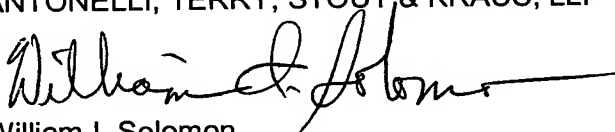
In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR 1.97 and 1.98 have been satisfied, in connection with all

documents on the enclosed List. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 501.36384CC9) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

A handwritten signature in black ink, appearing to read "William I. Solomon", with a long horizontal flourish extending to the right.

William I. Solomon
Registration No. 28,565

1300 North Seventeenth Street
Suite 1800
Arlington, VA 22209
Tel.: 703-312-6600
Fax.: 703-312-6666
WIS/sjg

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| Form PTO-1449 | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | ATTY. DKT. NO. 501.36384CC9 | SERIAL NO. TBD |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) | | APPLICANT FUJISAWA, et al. | |
| | | FILING DATE July 3, 2003 | GROUP 2814 |

U.S. PATENT DOCUMENTS

| Examiner Initial | Document Number | Date | Name | Class | Subclass | Filing Date |
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| | AB | 5,874,784 | 2-99 | Aoki, et al. | | |
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| | AD | 5,917,234 | 6-99 | Tsuruzono | | |
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| | AF | 5,590,462 | 1-97 | Hundt, et al. | | |
| | AG | 5,886,409 | 3-99 | Ishino, et al. | | |
| | AH | 5,592,025 | 1-97 | Clark, et al. | | |
| | AI | | | | | |
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| | AK | | | | | |
| | AL | | | | | |

FOREIGN PATENT DOCUMENTS

| Examiner Initial | Document Number | Date | Country | Class | Subclass | Translation /Abstract | |
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| | | | | | | Yes | No |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | | |
|-----------------|----|--|
| | AU | "Various Types of BGA Packages that Seal 100-MHz LSIs", <u>Nikkei Electronics</u> , Feb. 28, 1994. |
| | AV | |
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| | AX | |
| | AY | |
| | AZ | |
| Examiner | | Date Considered |